

**=> IFW: Scan as Doc Code: SRNT <=
 Doc Date:**

TC 3700 Inventor Search Program

See attached inventor searches for applications and/or patents to help resolve questions of overlapping subject matter. These searches are provided as an initial examination aid: examiners should perform updated or expanded PALM or EAST inventors searches as appropriate.

Serial Number: 10801364

**1.) See attached printout of inventors listed in
PALM**

**2.) See attached EAST Inventor Search
Printout shows Inventor search terms**

PALM INTRANET

Day : Friday
Date: 10/20/2006
Time: 13:41:43

Inventor Information for 10/801364

Inventor Name	City	State/Country
HUANG, QINGFA	XIAMEN	CHINA
LIN, FENGXI	XIAMEN	CHINA

[Appln Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity/Reexam](#)[Foreign Data](#)[Invento](#)

Search Another: Application#

[Search](#)

or Patent#

[Search](#)

PCT /

[Search](#)

or PG PUBS #

[Search](#)

Attorney Docket #

[Search](#)

Bar Code #

[Search](#)

To go back use Back button on your browser toolbar.

[Back to PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

US 20060231674 A1	20061019	Movable lifting stand	242/615.2		Lin; Feng-Hsu
US 20060228845 A1	20061012	Method for pre-retaining CB opening	438/197		Chen; Yinan et al.
US 20060216349 A1	20060928	IBUPROFEN COMPOSITION	424/464	424/440; 514/570	Bunick; Frank J. et al.
US 20060203948 A1	20060914	Phase detector and method providing rapid locking of delay-lock loops	375/375	375/376	Lin; Feng
US 20060202726 A1	20060914	Fast-locking digital phase locked loop	327/141		Lin; Feng et al.
US 20060198237 A1	20060907	Method and apparatus for initialization of read latency tracking circuit in high-speed DRAM	365/233		Johnson; James Brian et al.
US 20060195713 A1	20060831	Duty cycle distortion compensation for the data output of a memory device	713/400		Johnson; James B. et al.
US 20060170637 A1	20060803	LCD panel and LCD device	345/96		Lin; Feng-Li
US 20060161716 A1	20060720	Adapter for connecting a portable memory unit to a host, and a memory device having the adapter	710/313		Lin; Feng Hsi
US 20060143491 A1	20060629	Memory system and method for strobing data, command and address signals	713/600	365/189.05; 365/233; 713/400; 713/500	Lin; Feng et al.
US 20060140023 A1	20060629	Memory system and method for strobing data, command and address signals	365/193		Lin; Feng et al.
US 20060133165 A1	20060622	Memory system and method for strobing data, command and address signals	365/193		Lin; Feng et al.
US 20060127764	20060615	Electrochemical cells electrodes and binder	429/217	429/206	Chen; Muguo et

A1		materials for electrochemical cells electrodes			al.
US 20060126406 A1	20060615	Memory system and method for strobing data, command and address signals	365/193		Lin; Feng et al.
US 20060120371 A1	20060608	Coordinated convergecast for AD HOC wireless networks	370/392		Huang; Qingfeng et al.
US 20060120103 A1	20060608	Liquid-crystal display apparatus and backlight module thereof	362/561	362/330; 362/559; 362/560	Lin; Feng-Li
US 20060119396 A1	20060608	Skew tolerant high-speed digital phase detector	327/3		Lin; Feng
US 20060098709 A1	20060511	Infrared thermometer	374/121		Huang; Horng-Tsann et al.
US 20060083126 A1	20060420	Methods and apparatuses of half-wave layer jump control for optical disk drive	369/44.29	369/44.27; 369/53.28	Lin; Feng-Fu et al.
US 20060082401 A1	20060420	Measure-controlled delay circuits with reduced phase error	327/142		Lin; Feng
US 20060062058 A1	20060323	Interleaved delay line for phase locked and delay locked loops	365/194		Lin; Feng
US 20060058670 A1	20060316	Method and apparatus for ultrasound spatial compound imaging with adjustable aperture controls	600/447		Lin; Feng et al.
US 20060058172 A1	20060316	Method for manufacturing DP-bioglass composition for use in dental fracture repair	501/63	501/70; 501/73	Lin; Feng Huei
US 20060047982 A1	20060302	Multi-interface auto-switch circuit and memory device with dual interface auto-switch circuit	713/300		Lo; Hai Cha et al.

US 20060047861 A1	20060302	Method for soft configuring communication protocols	710/11		Hung; Jeng Shin et al.
US 20060047860 A1	20060302	Flash memory device with a plurality of protocols and a method for controlling the flash memory device	710/11		Lin; Feng Hsi
US 20060044891 A1	20060302	Memory system and method for strobing data, command and address signals	365/193		Lin; Feng et al.
US 20060033542 A1	20060216	Fast-locking digital phase locked loop	327/156		Lin; Feng et al.
US 20060030776 A1	20060209	Range dependent weighting for spatial compound imaging	600/437		Washburn; Michael Joseph et al.
US 20060028905 A1	20060209	Method and apparatus for initialization of read latency tracking circuit in high-speed DRAM	365/233		Johnson; James Brian et al.
US 20060017480 A1	20060126	Method and apparatus to set a tuning range for an analog delay	327/158		Lin; Feng
US 20060012410 A1	20060119	Measure-controlled delay circuits with reduced phase error	327/161		Lin; Feng
US 20060007616 A1	20060112	Power supply voltage detection circuitry and methods for use of the same	361/90		Pan; Dong et al.
US 20050286672 A1	20051229	Phase splitter using digital delay locked loops	375/376		Lin, Feng et al.
US 20050285841 A1	20051229	Video display driving method of an LCD	345/102		Lin, Feng-Li
US 20050285626 A1	20051229	Circuits and methods of temperature compensation for refresh oscillator	326/93		Lin, Feng
US 20050285606	20051229	Delay lock circuit having self-calibrating	324/601		Lin, Feng

A1		loop			
US 20050285605 A1	20051229	Delay lock circuit having self-calibrating loop	324/601	324/76.54	Lin, Feng
US 20050280479 A1	20051222	Circuits and methods of temperature compensation for refresh oscillator	331/185		Lin, Feng
US 20050280456 A1	20051222	Phase detector for reducing noise	327/156		Lin, Feng
US 20050270893 A1	20051208	Phase detector for all-digital phase locked and delay locked loops	365/233		Lin, Feng et al.
US 20050264331 A1	20051201	Methods and apparatus for delay circuit	327/161		Lin, Feng et al.
US 20050255339 A1	20051117	Metal air cell system	429/12	427/115; 429/33; 429/40	Tsai, Tsepin et al.
US 20050242888 A1	20051103	Phase-locked loop circuits with reduced lock time	331/16		Lee, Seong-hoon et al.
US 20050240791 A1	20051027	Interleaved delay line for phase locked and delay locked loops	713/401		Lin, Feng
US 20050225368 A1	20051013	Phase detector for reducing noise	327/161		Lin, Feng
US 20050207586 A1	20050922	Mobile communication earphone accommodating hearing aid with volume adjusting function and method thereof	381/74	455/575.2	Lin, Feng-Chi et al.
US 20050190667 A1	20050901	Long Seek control device for optical disk drive and method thereof	369/44.28	369/30.17	Lin, Feng-Fu et al.
US 20050184739 A1	20050825	Delay lock circuit having self-calibrating loop	324/601		Lin, Feng
US 20050179172 A1	20050818	Method for preparing porous bioceramic bone substitute	264/643		Lin, Feng-Huei et al.

		materials			
US 20050158972 A1	20050721	Method for manufacturing bit line contact structure of semiconductor memory	438/586		Lin, Feng-Chuan et al.
US 20050140407 A1	20050630	System and method to improve the efficiency of synchronous mirror delays and delay locked loops	327/153		Lin, Feng
US 20050134337 A1	20050623	Clock signal distribution with reduced parasitic loading effects	327/158		Lee, Seong-hoon et al.
US 20050122153 A1	20050609	Centralizing the lock point of a synchronous circuit	327/291		Lin, Feng Dan
US 20050116751 A1	20050602	Method and apparatus for improving stability and lock time for synchronous circuits	327/158		Lin, Feng et al.
US 20050101865 A1	20050512	Method and apparatus for ultrasound compound imaging with combined fundamental and harmonic signals	600/447		Hao, Xiaohui et al.
US 20050099880 A1	20050512	Duty cycle distortion compensation for the data output of a memory device	365/233		Johnson, James B. et al.
US 20050083099 A1	20050421	Capture clock generator using master and slave delay locked loops	327/295		Lin, Feng
US 20050081626 A1	20050421	METHOD FOR DETECTING AN UNBALANCED DISC	73/460		Lin, Feng-Fu et al.
US 20050077975 A1	20050414	Circuits and methods of temperature compensation for refresh oscillator	331/185		Lin, Feng
US 20050058871 A1	20050317	Oxygen separation through hydroxide-conductive membrane	429/30	204/270	Li, Lin-Feng et al.
US	20050303	Optical medium	250/201.5	369/53.22	Lin, Feng

20050045800 A1		judging method			Fu et al.
US 20050024108 A1	20050203	System and method to improve the efficiency of synchronous mirror delays and delay locked loops	327/158		Lin, Feng
US 20050017810 A1	20050127	Phase detector for reducing noise	331/25		Lin, Feng
US 20040232186 A1	20041125	Backpack with umbrella support	224/576	190/102	Huang, Qingfa et al.
US 20040223367 A1	20041111	Phase detector for all-digital phase locked and delay locked loops	365/154		Lin, Feng et al.
US 20040213074 A1	20041028	Method and apparatus for establishing and maintaining desired read latency in high-speed DRAM	365/233		Johnson, Brian et al.
US 20040213051 A1	20041028	METHOD AND APPARATUS FOR IMPROVING STABILITY AND LOCK TIME FOR SYNCHRONOUS CIRCUITS	365/194		Lin, Feng et al.
US 20040209422 A1	20041021	SEMICONDUCTOR DEVICE HAVING TRENCH TOP ISOLATION LAYER AND METHOD FOR FORMING THE SAME	438/242		Chen, Yi-Nan et al.
US 20040174185 A1	20040909	Method and apparatus for calibrating driver impedance	326/30		Lin, Feng et al.
US 20040158757 A1	20040812	Interleaved delay line for phase locked and delay locked loops	713/401		Lin, Feng
US 20040130366 A1	20040708	METHOD AND SYSTEM FOR DELAY CONTROL IN SYNCHRONIZATION	327/158		Lin, Feng et al.

		CIRCUITS			
US 20040127795 A1	20040701	Method of ultrasonic speckle reduction using wide-band frequency compounding with tissue-generated harmonics	600/443		Lin, Feng et al.
US 20040120211 A1	20040624	Interleaved delay line for phase locked and delay locked loops	365/232		Lin, Feng
US 20040119512 A1	20040624	Interleaved delay line for phase locked and delay locked loops	327/156		Lin, Feng
US 20040109889 A1	20040610	Surface treatment composition for soft substrates	424/465		Bunick, Frank J. et al.
US 20040048924 A1	20040311	Ibuprofen composition	514/557		Bunick, Frank J. et al.
US 20040041609 A1	20040304	Methods and apparatus for duty cycle control	327/175		Lin, Feng
US 20040033471 A1	20040219	Method for manufacturing the DP-bioglass to use in dental fracture repair via the carbon dioxide lasre	433/228.1		Lin, Feng Huei
US 20040013742 A1	20040122	Biodegradable ceramic for medical use	424/602	106/35; 501/1	Lin, Feng-Huei et al.
US 20030231042 A1	20031218	Methods and apparatus for delay circuit	327/276		Lin, Feng et al.
US 20030227305 A1	20031211	Method and apparatus for enabling a timing synchronization circuit	327/141		Mikhalev, Vladimir et al.
US 20030209167 A1	20031113	Dental retrograde-filling material containing cobalt and process	106/35		Lin, Feng-Huei et al.
US 20030154294 A1	20030814	Multi-stage email intreception method	709/229		Lin, Feng-Hui
US 20030099872	20030529	SOLID GEL MEMBRANE	429/27	359/270; 429/29;	CHEN, MUGUO

A1				429/30; 429/303; 429/33	et al.
US 20030081473 A1	20030501	System and method for skew compensating a clock signal and for capturing a digital signal using the skew compensated clock signal	365/200		Lin, Feng
US 20030081472 A1	20030501	System and method for skew compensating a clock signal and for capturing a digital signal using the skew compensated clock signal	365/200		Lin, Feng
US 20030065439 A1	20030403	Adaptive pid control method and system for internal combustion engine rotation speed pulsation damping	701/110	123/352; 123/436; 701/111	Lin, Feng et al.
US 20030054246 A1	20030320	Zinc anode for electrochemical cells	429/214	429/229; 549/429; 549/497	Li, Lin- Feng et al.
US 20030048648 A1	20030313	Current sense and control of DC/DC converters	363/65		Lin, Feng et al.
US 20030048643 A1	20030313	Method and circuit for start up in a power converter	363/21.06		Lin, Feng et al.
US 20030043685 A1	20030306	SYSTEM AND METHOD FOR SKEW COMPENSATING A CLOCK SIGNAL AND FOR CAPTURING A DIGITAL SIGNAL USING THE SKEW COMPENSATED CLOCK SIGNAL	365/233		Lin, Feng
US 20030043615 A1	20030306	SYSTEM AND METHOD FOR SKEW COMPENSATING A CLOCK SIGNAL AND FOR	365/141		Lin, Feng

		CAPTURING A DIGITAL SIGNAL USING THE SKEW COMPENSATED CLOCK SIGNAL			
US 20030034815 A1	20030220	Method to improve the efficiency of synchronous mirror delays and delay locked loops	327/158		Lin, Feng
US 20030022047 A1	20030130	Solid gel membrane	429/29	429/144; 429/206; 429/229; 429/27; 429/303	Chen, Muguo et al.
US 20020193284 A1	20021219	Methods for identifying modulators of NF-KB activity	514/1	435/18	Chen, Lin- feng et al.
US 20020180501 A1	20021205	Digital dual-loop DLL design using coarse and fine loops	327/158		Baker, R. Jacob et al.
US 20020172314 A1	20021121	Phase splitter using digital delay locked loops	375/376		Lin, Feng et al.
US 20020157031 A1	20021024	Capture clock generator using master and slave delay locked loops	713/401		Lin, Feng
US 20020142203 A1	20021003	Refuelable metal air electrochemical cell and refuelabel anode structure for electrochemical cells	429/27	429/212; 429/246	Ma, Fuyuan et al.
US 20020142202 A1	20021003	Fibrous electrode for a metal air electrochemical cell	429/27	429/217; 429/218.1; 429/221; 429/229; 429/231.6; 429/231.95; 429/232; 429/40	Li, Lin- Feng et al.
US 20020133731 A1	20020919	Duty cycle distortion compensation for the data output of a memory device	713/400		Johnson, James B. et al.
US	20020801	Solid gel membrane	429/303	429/206;	Chen,

20020102465 A1		separator in electrochemical cells		429/27; 429/9	Muguo et al.
US 20020089048 A1	20020711	Multi-chip semiconductor package structure	257/678	257/E23.039; 257/E23.042; 257/E23.052	Liao, Kuang-Ho et al.
US 20020074638 A1	20020620	MULTI-CHIP SEMICONDUCTOR PACKAGE STRUCTURE	257/686	257/724; 257/E23.039; 257/E23.042; 257/E23.052	Liao, Kuang-Ho et al.
US 20010030127 A1	20011018	Oxygen separation through hydroxide- conductive membrane	204/252	204/296; 205/633; 521/27	Li, Lin- Feng et al.
US 20010025332 A1	20010927	Crossbar integrated circuit with parallel channels for a communication device	710/317		Wang, Yuanlong et al.
US 7126874 B2	20061024	Memory system and method for strobing data, command and address signals	365/233	365/189.05; 365/193; 365/230.01; 365/230.08	Lin; Feng et al.
US 7123525 B2	20061017	Phase detector for all- digital phase locked and delay locked loops	365/194	365/189.07; 365/233	Lin; Feng et al.
US 7109807 B2	20060919	Phase detector for reducing noise	331/25	327/161; 327/7	Lin; Feng
US 7106603 B1	20060912	Switch-mode self- coupling auxiliary power device	363/21.08	363/21.16; 363/97	Lin; Feng- Hsu et al.
US 7103791 B2	20060905	Interleaved delay line for phase locked and delay locked loops	713/401	327/147; 327/149; 327/182; 327/183; 365/194; 365/233; 375/371; 375/373; 711/100; 711/105; 711/167; 711/170; 713/400; 713/500; 713/503	Lin; Feng
US 7102361 B2	20060905	Delay lock circuit having self-calibrating loop	324/601	324/76.54	Lin; Feng

US 7098714 B2	20060829	Centralizing the lock point of a synchronous circuit	327/291	327/158	Lin; Feng Dan
US 7081343 B2	20060725	Methods for identifying modulators of NF-KB activity	435/7.1	435/18; 435/29	Chen; Lin-feng et al.
US 7078053 B2	20060718	Ibuprofen composition	424/441		Bunick; Frank J. et al.
US 7065001 B2	20060620	Method and apparatus for initialization of read latency tracking circuit in high-speed DRAM	365/233	365/194; 365/236	Johnson; James Brian et al.
US 7058688 B2	20060606	Multi-stage email interception method	709/206	707/3; 707/6; 709/207	Lin; Feng-Hui
US 7028208 B2	20060411	Duty cycle distortion compensation for the data output of a memory device	713/401	327/158; 327/159; 365/233; 713/400; 713/500; 713/501	Johnson; James B. et al.
US 7028134 B2	20060411	Crossbar integrated circuit with parallel channels for a communication device	710/317	370/360; 710/316	Wang; Yuanlong et al.
US 7020794 B2	20060328	Interleaved delay line for phase locked and delay locked loops	713/401	307/141; 307/409; 327/149; 327/152; 327/153; 327/158; 327/182; 365/194; 365/233; 370/509; 370/517; 370/519; 713/400; 713/500; 713/503; 713/600	Lin; Feng
US 7009407 B2	20060307	Delay lock circuit having self-calibrating loop	324/601	324/76.54	Lin; Feng
US D515935 S	20060228	Cosmetics container	D9/663	D9/558; D9/661	Lin; Feng-Yu

US 6992534 B2	20060131	Circuits and methods of temperature compensation for refresh oscillator	331/176	331/177R; 331/177V; 331/186; 331/57	Lin; Feng
US 6987701 B2	20060117	Phase detector for all-digital phase locked and delay locked loops	365/194	327/156; 327/158; 365/233	Lin; Feng et al.
US 6950487 B2	20050927	Phase splitter using digital delay locked loops	375/376		Lin; Feng et al.
US 6940328 B2	20050906	Methods and apparatus for duty cycle control	327/175	327/172	Lin; Feng
US 6930955 B2	20050816	Method and apparatus for establishing and maintaining desired read latency in high-speed DRAM	365/233	365/194; 365/236	Johnson; Brian et al.
US 6930525 B2	20050816	Methods and apparatus for delay circuit	327/161	327/276	Lin; Feng et al.
US 6912666 B2	20050628	Interleaved delay line for phase locked and delay locked loops	713/401	711/100; 711/105; 711/167; 711/170; 713/400; 713/500; 713/503; 713/600	Lin; Feng
US 6895522 B2	20050517	Method and apparatus for compensating duty cycle distortion in a data output signal from a memory device by delaying and distorting a reference clock	713/401	365/233; 713/400; 713/600; 713/601	Johnson; James B. et al.
US 6891415 B2	20050510	Method and apparatus for enabling a timing synchronization circuit	327/158	327/161	Mikhalev; Vladimir et al.
US 6872619 B2	20050329	Semiconductor device having trench top isolation layer and method for forming the same	438/242	257/300; 257/302; 438/243	Chen; Yi-Nan et al.
US 6868504 B1	20050315	Interleaved delay line for phase locked and delay locked loops	713/500	307/141; 307/409; 365/194; 365/233;	Lin; Feng

				370/508; 370/517; 370/519; 704/502; 713/401; 713/503	
US 6845459 B2	20050118	System and method to provide tight locking for DLL and PLL with large range, and dynamic tracking of PVT variations using interleaved delay lines	713/401	327/147; 327/149; 327/157; 327/158; 327/161; 711/100; 711/105; 713/400; 713/500; 713/503	Lin; Feng
US 6845458 B2	20050118	System and method of operation of DLL and PLL to provide tight locking with large range, and dynamic tracking of PVT variations using interleaved delay lines	713/401	327/147; 327/149; 327/157; 327/158; 327/161; 713/400; 713/500; 713/503	Lin; Feng
US 6844616 B2	20050118	Multi-chip semiconductor package structure	257/676	257/E23.039; 257/E23.042; 257/E23.052	Liao; Kuang-Ho et al.
US 6839860 B2	20050104	Capture clock generator using master and slave delay locked loops	713/401	713/503	Lin; Feng
US 6839301 B2	20050104	Method and apparatus for improving stability and lock time for synchronous circuits	365/233	327/141; 327/142; 327/154; 327/155; 327/156; 327/158; 327/159; 365/194	Lin; Feng et al.
US 6836166 B2	20041228	Method and system for delay control in synchronization circuits	327/158	327/161; 327/276; 327/299	Lin; Feng et al.
US 6827685 B2	20041207	Method of ultrasonic speckle reduction using wide-band frequency	600/437		Lin; Feng et al.

		compounding with tissue-generated harmonics			
US 6812753 B2	20041102	System and method for skew compensating a clock signal and for capturing a digital signal using the skew compensated clock signal	327/155	327/156; 327/158; 327/159	Lin; Feng
US 6798259 B2	20040928	System and method to improve the efficiency of synchronous mirror delays and delay locked loops	327/153	327/299; 365/233	Lin; Feng
US 6794069 B1	20040921	Fuel cell support and electrical interconnector	429/17	429/123; 429/152; 429/9	Faris; Sadeg M. et al.
US 6779126 B1	20040817	Phase detector for all-digital phase locked and delay locked loops	713/500	327/147; 327/156; 327/160; 327/166; 713/400; 713/502; 713/503; 713/600	Lin; Feng et al.
US 6774690 B2	20040810	Digital dual-loop DLL design using coarse and fine loops	327/158	327/156; 327/278; 375/376	Baker; R. Jacob et al.
US 6767663 B2	20040727	Oxygen separation through hydroxide-conductive membrane	429/33	429/19; 429/303; 521/27	Li; Lin- Feng et al.
US 6762974 B1	20040713	Method and apparatus for establishing and maintaining desired read latency in high-speed DRAM	365/233	365/194; 365/236	Johnson; Brian et al.
US 6760235 B2	20040706	Soft start for a synchronous rectifier in a power converter	363/21.06	363/49	Lin; Feng et al.
US 6759882 B2	20040706	System and method for skew compensating a clock signal and for capturing a digital signal using the skew compensated clock	327/155	327/156; 327/158; 327/159	Lin; Feng

		signal			
US 6699189 B1	20040302	Ultrasound distortion compensation using blind system identification	600/437		Lin; Feng et al.
US 6687185 B1	20040203	Method and apparatus for setting and compensating read latency in a high speed DRAM	365/233	327/147; 327/156; 327/158; 365/194	Keeth; Brent et al.
US 6653003 B1	20031125	Fuel cell support and electrical interconnector	429/17	429/123; 429/152; 429/18; 429/27; 429/9	Tsai; Tsepin et al.
US 6627214 B1	20030930	Ibuprofen composition	424/441	424/439; 424/469; 424/484; 514/784; 514/974	Bunick; Frank J. et al.
US 6618283 B2	20030909	System and method for skew compensating a clock signal and for capturing a digital signal using the skew compensated clock signal	365/141	365/155; 365/158; 365/161	Lin; Feng
US 6611475 B2	20030826	System and method for skew compensating a clock signal and for capturing a digital signal using the skew compensated clock signal	365/233	365/194	Lin; Feng
US 6605391 B2	20030812	Solid gel membrane	429/303	429/29	Chen; Muguo et al.
US 6591808 B2	20030715	Adaptive PID control method and system for internal combustion engine rotation speed pulsation damping	123/352	123/192.1; 123/339.21; 123/436; 701/110; 701/111	Lin; Feng et al.
US 6574124 B2	20030603	Plural power converters with individual conditioned error signals shared on a	363/65	363/72; 363/97	Lin; Feng et al.

		current sharing bus			
US 6541159 B1	20030401	Oxygen separation through hydroxide-conductive membrane	429/236	204/295; 205/629; 429/235	Li; Lin-Feng et al.
US 6469988 B1	20021022	Low-level circuit implementation of signal flow graphs for real-time signal processing of high-speed digital signals	370/289	370/296; 375/220; 375/346	Yang; Kewei et al.
US 6458617 B1	20021001	Multi-chip semiconductor package structure	438/51	257/E23.039; 257/E23.042; 257/E23.052; 438/108; 438/123; 438/55	Liao; Kuang-Ho et al.
US 6445231 B1	20020903	Digital dual-loop DLL design using coarse and fine loops	327/158	327/278; 375/376	Baker; R. Jacob et al.
US 6418537 B1	20020709	Accurate timing calibration for each of multiple high-speed clocked receivers using a single DLL	713/400	710/60; 713/503	Yang; Kewei et al.
US 6358651 B1	20020319	Solid gel membrane separator in rechargeable electrochemical cells	429/303	29/623.1	Chen; Muguo et al.
US 6338752 B1	20020115	.alpha.-TCP/HAP biphasic cement and its preparing process	106/35	106/670; 423/305; 423/311; 423/315; 501/1	Lin; Feng-Huei et al.
US 6310410 B1	20011030	Method and apparatus for reducing source voltage deviation in hot plug applications	307/119	307/135; 307/147; 361/58	Lin; Feng et al.
US 6226696 B1	20010501	Programmable circuit for sensing computer pointing devices to sink a different amount of current depending on the requirements of the device	710/8	345/161; 345/163; 710/14	Lin; Feng-Chen et al.
US 6224629 B1	20010501	Bone substitute composition and	623/16.11	623/17.11	Lin; Feng-Huei et al.

		process of preparation thereof			
US 6181577 B1	20010130	Auxiliary bias circuit for a power supply and a method of operation thereof	363/21.01	363/141	Chen; Shiaw-Jong Steve et al.
US 6136030 A	20001024	Process for preparing porous bioceramic materials	128/898	514/324; 523/113; 623/919	Lin; Feng-Huei et al.
US 6085555 A	20000711	Palm-top fabric leading edge detector	68/13R	68/213; 68/235R	Wu; Jong-Fu et al.
US 6081436 A	20000627	Multi-output power supply voltage sensing	363/65		Lin; Feng
US 6061260 A	20000509	Board mounted power supply having an auxiliary output	363/141	257/E23.099; 361/687	Chen; Shiaw-Jong Steve et al.
US 6055169 A	20000425	Current mode control circuit for paralleled power supply and method of operation thereof	363/65	363/71	Bowman; Wayne C. et al.
US 6005674 A	19991221	System architecture for multiple input/output devices	358/437	358/434; 358/468; 379/100.01; 379/100.12; 710/260	Lin; Feng
US 5881064 A	19990309	Packet-switched data network and method of operation	370/389	358/426.1	Lin; Feng et al.
US 5730806 A	19980324	Gas-liquid supersonic cleaning and cleaning verification spray system	134/22.12	134/102.1; 134/102.2; 134/113; 134/36	Caimi; Raoul E. B. et al.
US 5687589 A	19971118	Batch dyeing system with automatic chemical-dosing and pH control system	68/12.07	68/207	Yao; Shin-Chuan et al.
US 5546388 A	19960813	Packet-switched facsimile network and method of operation	370/389	359/425	Lin; Feng
US 5364024 A	19941115	Application independent, portable room temperature and humidity controller	236/44C	165/229; 62/176.6	Lin; Feng
US D337392	19930713	Table lamp	D26/106		Lin; Feng-

S					Jye
US D336534 S	19930615	Lamp head	D26/63	D26/128	Lin; Feng-Jye
US 5200254 A	19930406	Receptor sheet manifolds for thermal mass transfer imaging	428/32.39	428/323; 428/331; 428/913; 428/914	Henry; Robert M. et al.
US D330266 S	19921013	Adjustable lamp	D26/65		Lin; Feng-Jye
US 4813463 A	19890321	Instantly applicable correction fluid container structure	141/351	141/346; 222/518; 401/260; 401/264	Lin; Feng-Yu
US 4202472 A	19800513	Dispenser for insecticide or the like	222/187	206/205; 206/222; 206/532; 383/40	Lin; Feng
US 4105144 A	19780808	Dispenser for insect or vermin poison	222/187	206/205; 206/222; 206/532; 222/541.9; 383/205; 383/40; 428/907	Lin; Feng